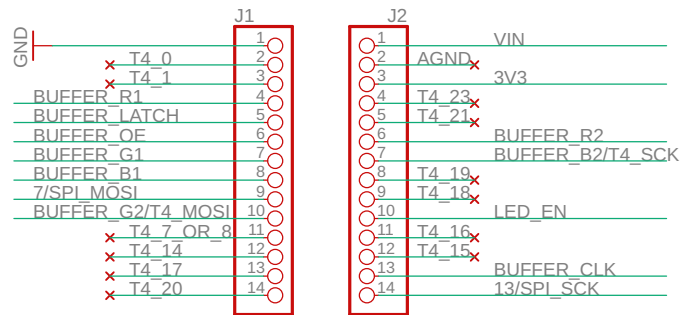
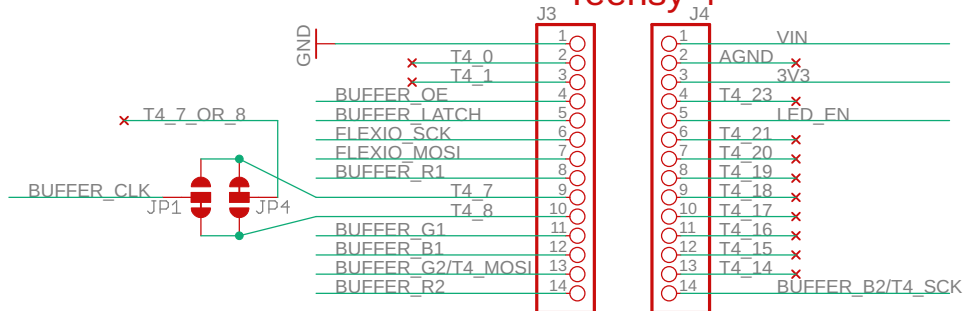


Teensy 3



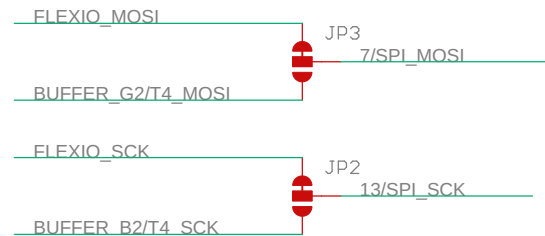
All pins marked with X are unused by SmartLED Library

Teensy 4



Default: Teensy pin 8 to BUFFER_CLK, Audio Shield DOUT (to Shield) free, DIN (to Teensy) blocked
Alt: Teensy pin 7 to BUFFER_CLK, Audio Shield DIN (to Teensy) free, DOUT (to Shield) blocked

APA102 Config



APA CONFIG OPTIONS (note: this is opposite of the prototype Teensy 4 shield)

Default: JP2 connects 4 to APA_CLK

JP3 connects 5 to APA_DAT

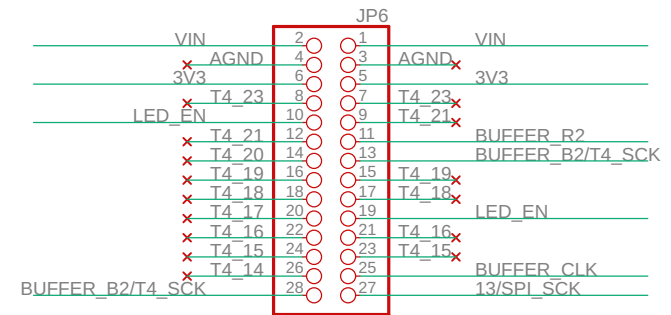
Can drive APA102 LEDs from GPIO or (possibly) FlexIO peripheral, and HUB75 at same time

Alt: JP2 connects 13 "SPI(4) SCK" and APA_CLK (Pin 4 free),

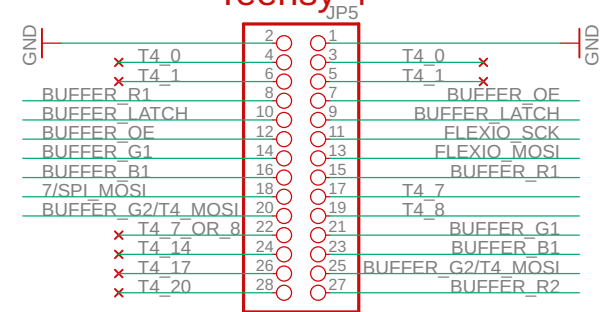
JP3 connects 11 "SPI(4) MOSI" and APA_DAT (Pin 5 free).

Must choose between driving HUB75 panel *OR* APA102 LEDs

Combined SMD Pinouts (duplicate of left pinouts)



Teensy 4



Teensy 4 to SmartLED Shield for Teensy 3 Adapter

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