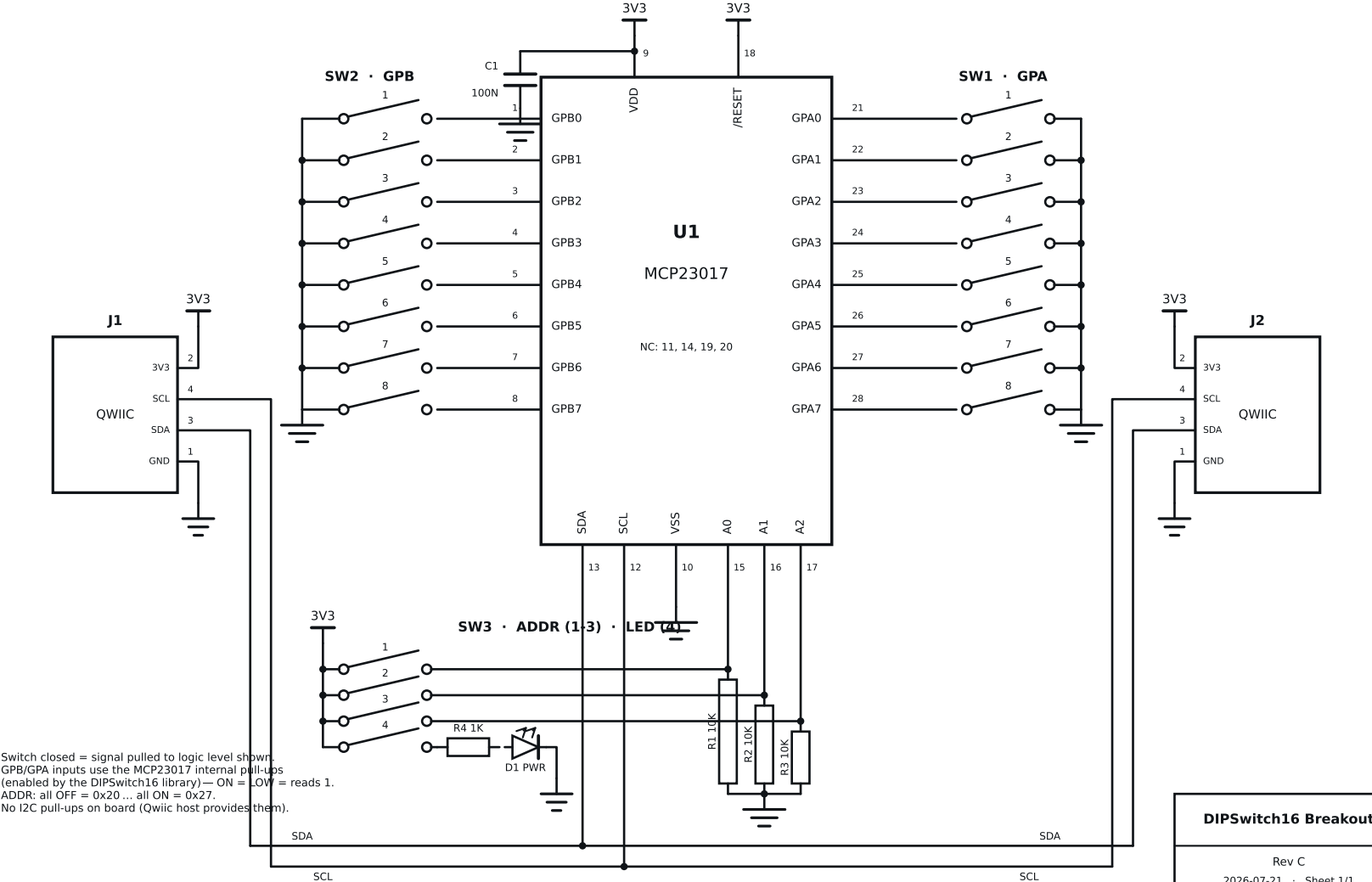


DIPSwitch16 — Schematic



Switch closed = signal pulled to logic level shown.
GPB/GPA inputs use the MCP23017 internal pull-ups
(enabled by the DIPSwitch16 library)— ON = LOW = reads 1.
ADDR: all OFF = 0x20 ... all ON = 0x27.
No I2C pull-ups on board (Qwiic host provides them).