

TYPICAL APPLICATION DATA

This application demonstrates how the displays may be cascaded for N-bit display applications. It features:

Synchronous, look-ahead counting

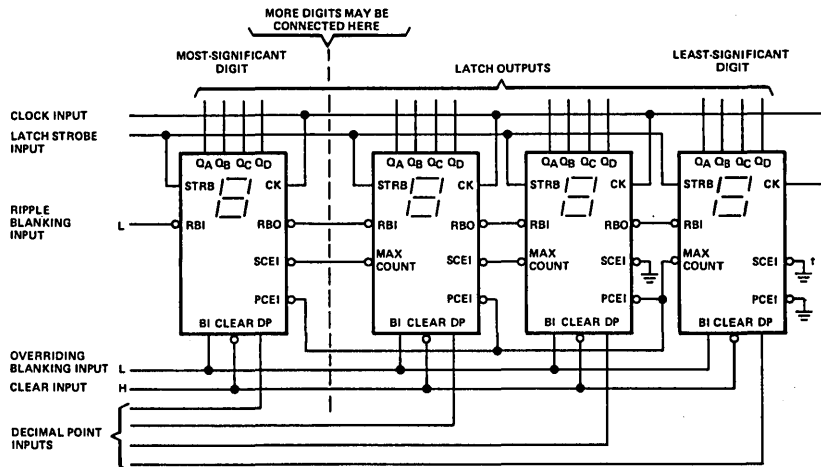
Ripple blanking for leading zeros

Overriding blanking for total suppression or intensity modulation of display

Direct parallel clear

Latch strobe permits counter to acquire data for the next display while viewing current display.

For other counter configurations, see Counting Circuits Using TIL306 and TIL308 LED's on page 257.



[†]The serial carry input of the least-significant digit is normally grounded; however, it may be used as a count-enable control for the entire counter (high to disable, low to count) provided the logic level on this pin is not changed while the clock line is low or false counting may result.