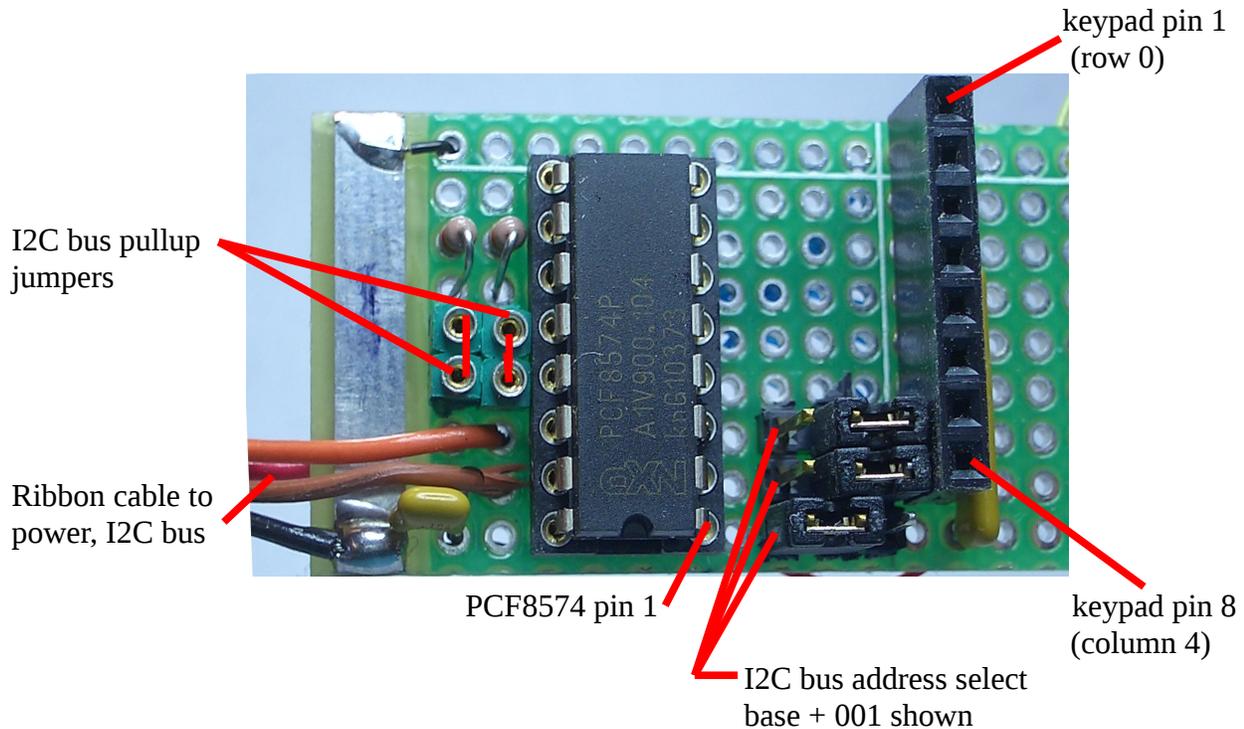


Datasheet for I2C keypad interface

December 2011

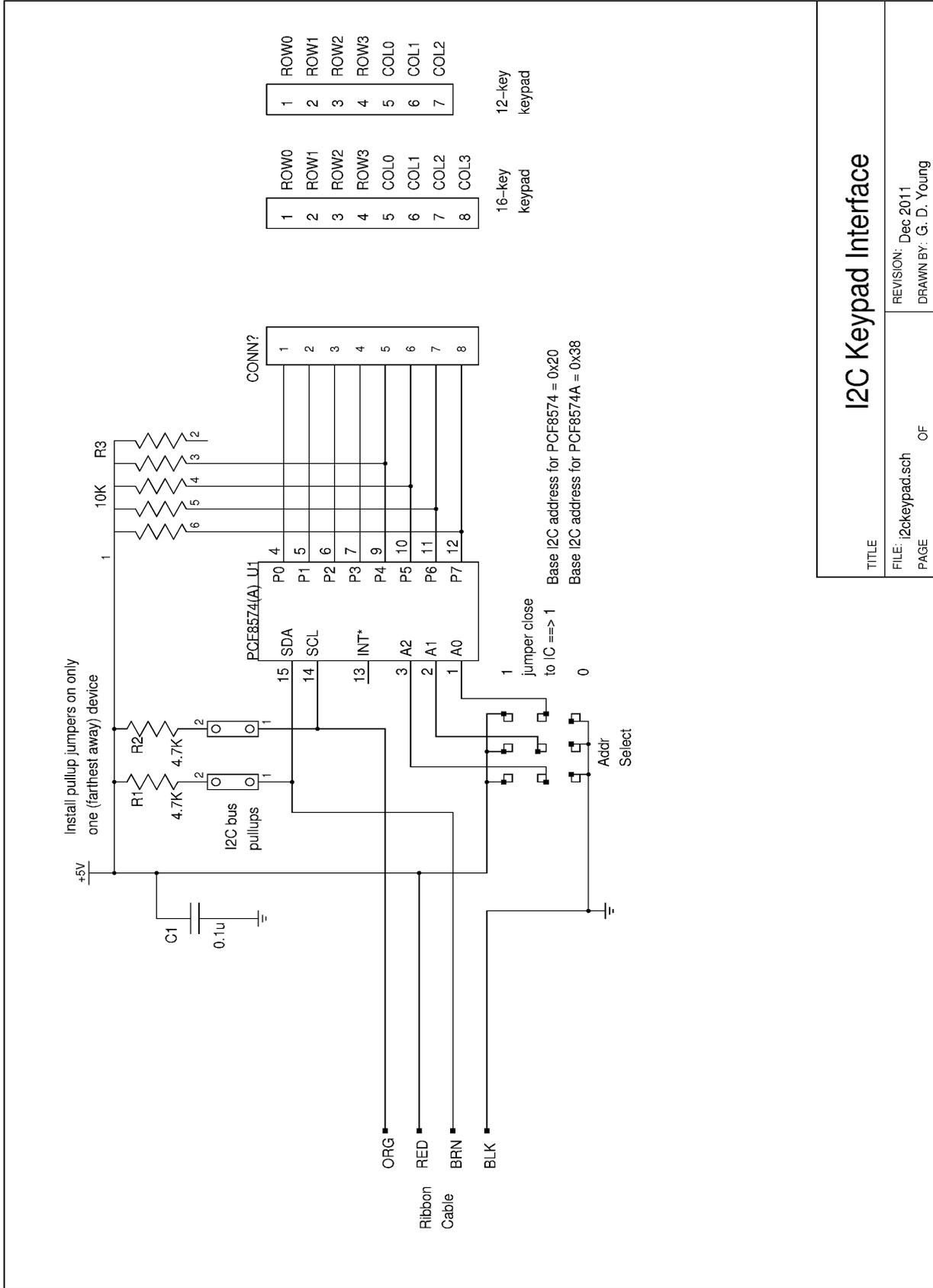
Board Layout



Notes

- The base address for the PCF8574P is 0x20, for the PCF8574AP it is 0x38. The two devices are otherwise completely interchangeable. The 3 LSB of the address are selected by the bus address select jumpers: the bit value is 1 when the jumper is closest to the IC, the LSB is next to the board edge.
- The I2C bus jumpers are only installed on one board/device in a system. Usually the jumpers are installed on the device that is physically the most distant from the bus master.
- When using a 12-key Digitran keypad, the column 4 socket position is unused, the rows and remaining 3 column pins are the same as for the 16-key keypad.

Schematic



I2C Keypad Interface

TITLE

REVISION: Dec 2011

DRAWN BY: G. D. Young

FILE: i2ckeypad.sch

PAGE OF